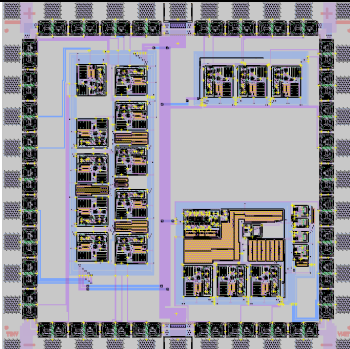
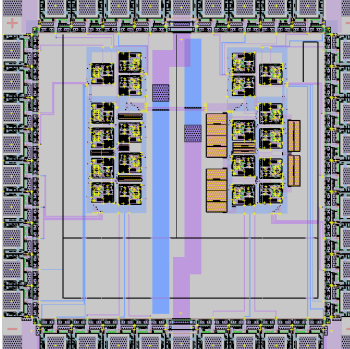
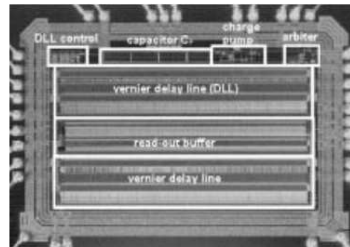
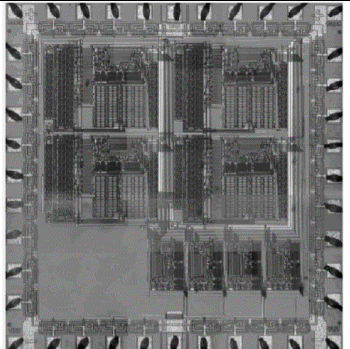
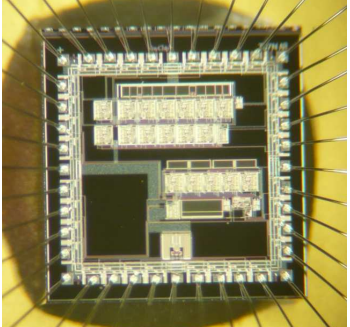
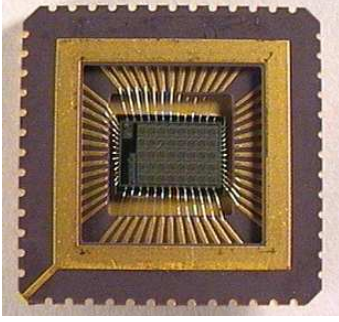
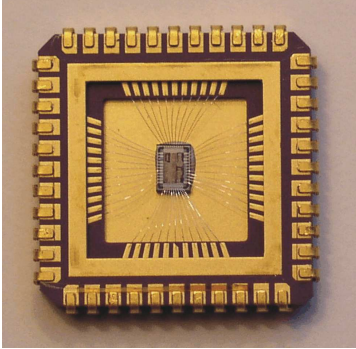
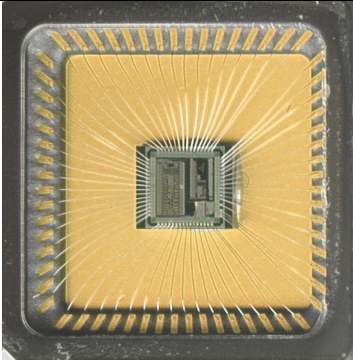
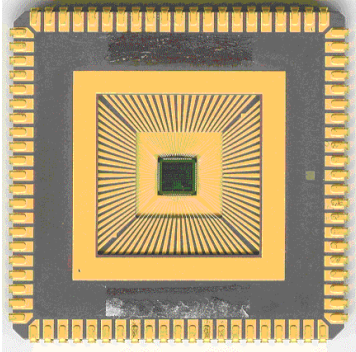

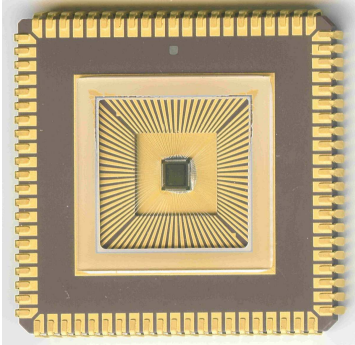
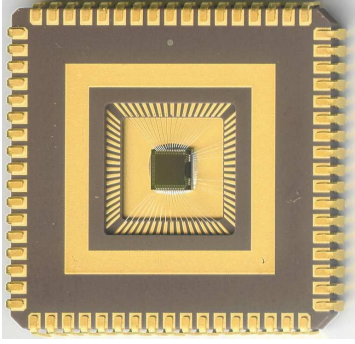
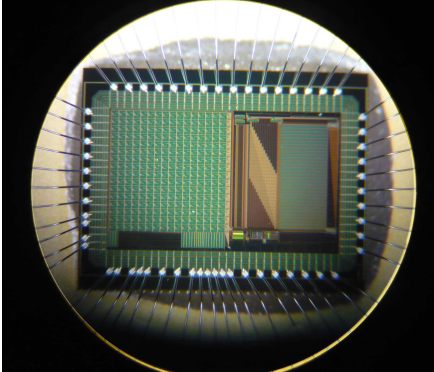
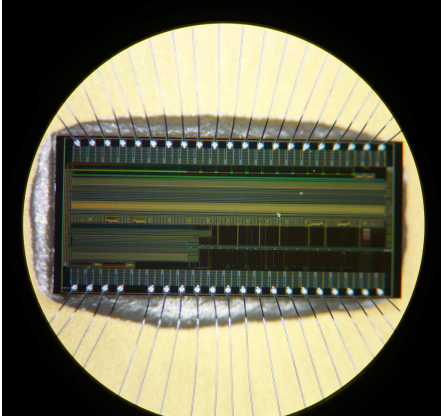
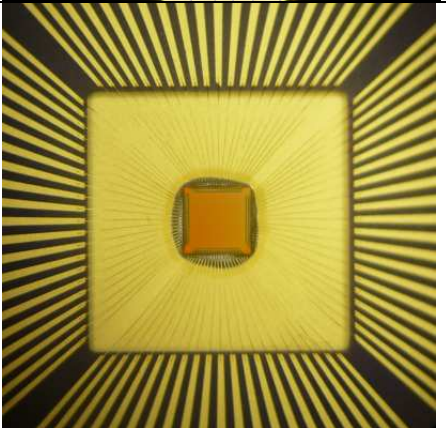
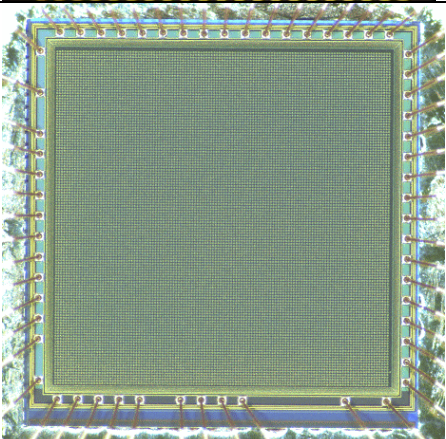


	Year	Technology	Description	Pictures	Tools	Designers
1	1995	CMOS 2um ORBIT	Set of tunable 3 rd order OTA-C filters with automatic tuning, 6-25MHz, 35mW, VDD=5V		Magic Layout Editor Simulators: Eldo PSPICE	J. Jakusz B. Pankiewicz S. Szczepański
2	1996	CMOS 1.2um ORBIT	Analog tunable OTA-C Filters, cut-off frequencies 20MHz, 100MHz, 40mW, VDD=5V		Magic Layout Editor Simulators: Eldo PSPICE	J. Jakusz B. Pankiewicz S. Szczepański
3	1999	CMOS 0.7um	Time to Digit Converter		Cadence	P. Dudek, S. Szczepański, J.Hatfield
4	1999	CMOS 2um ORBIT	Analog Programmable Matrix – 4 Configurable Blocks		Magic Layout Editor Simulators: Eldo PSPICE	B. Pankiewicz M. Wójcikowski J. Jakusz J. Glinianowicz S. Szczepański
5	2001	CMOS 1.5umAMI (AMlabn, MOSIS)	Baseband Filters for GSM Receiver		Magic Pspice	B. Pankiewicz, W. Jendernalik S. Szczepański

6	2000	CMOS 2um ORBIT	Analog Programmable Matrix – 4 Configurable Blocks		Magic Layout Editor Simulators: Eldo PSPICE	B. Pankiewicz M. Wójcikowski J. Jakusz J. Glinianowicz S. Szczepański
7	2001	BiCMOS 0.6um AMS	Functional Blocks of GSM Receiver: down conversion mixers, phase shifters, Active-RC filters), 900 MHz, VDD=3.3V		Cadence Virtuoso: LayoutPlus Spectre	G. Blakiewicz J. Jakusz S. Szczepański
8	2001	CMOS 0.8um AMS	Band-Pass Filters for GSM Receiver: tunable/ programmable OTA-C filters with cutoff frequency 150 – 300kHz, VDD=3.3V		Cadence Virtuoso: LayoutPlus Spectre	G. Blakiewicz J. Jakusz B. Pankiewicz S. Szczepański
9	2005	CMOS 0.35um AMS	Functional Blocks (band-pass filters, amplifiers) for Multi-System GSM Receiver tunable/ programmable OTA-C, Active-RC filters with cutoff frequency 100 – 700kHz, VDD=3.3V		Cadence Virtuoso: LayoutPlus Spectre	J. Jakusz W. Jendernalik S. Szczepański

10	2009	CMOS 130nm UMC	Specialized Multi- Processor Sensor Network Integrated Circuit 2.7M gates, 50MHz clock, 10 processors		Cadence Encounter: RTL Compiler ver. 6.2, Conformal, SOC Encounter GXL ver. 6,2	M. Wójcikowski B. Pankiewicz R. Żaglewski M. Kłósowski S. Szczepański
11	2009	CMOS 0.35um AMS	Fast Analog Processor for Real-Time Convolutd Image Procesing		Cadence Virtuoso: LayoutPlus Spectre	J. Jakusz, W. Jendernalik, G. Blakiewicz, R. Piotrowski S. Szczepański
12	2009	CMOS 0.35um	Low Power Analog Processor for Real-Time Convolutd Image Procesing		Cadence Virtuoso: LayoutPlus Spectre	J. Jakusz, W. Jendernalik, G. Blakiewicz, R. Piotrowski S. Szczepański
13	2011	CMOS 0.35um AMS	Analog Inter- Prediction Coprocesor for H264 Compression		Cadence Virtuoso IC6.1, Layout Suite XL, Schematic Editor XL, Spectre	J. Jakusz, W. Jendernalik, G. Blakiewicz, R. Piotrowski S. Szczepański

14	2012	CMOS 0.35um AMS	Analog Inter-Prediction Coprocessor for H.264 Video Compression		Cadence Virtuoso IC6.1, Layout Suite XL, Schematic Editor XL, Spectre	J. Jakusz, W. Jendernalik, G. Blakiewicz, R. Piotrowski, S. Szczepański
15	2012	CMOS 90nm	H.264 Compatible Video Compression Accelerator		Cadence Encounter: RTL Compiler v10.10, Conformal, SOC, Encounter v10.12	M. Wójcikowski, B. Pankiewicz, M. Kłosowski, S. Szczepański
16	2013	CMOS 180nm AMS 6M	Vison Chip 3			
17	2014	CMOS 180nm AMS	Vision Chip 4	